

1200V 100A Insulated Gate Bipolar Transistor

AKBK2A100YHH

Description:

Gen 2 IGBT with soft, fast recovery full current rated anti-parallel Emitter Controlled diode, providing ultra-low conduction loss. They are designed for applications such as UPS, inverters, etc.

Features:

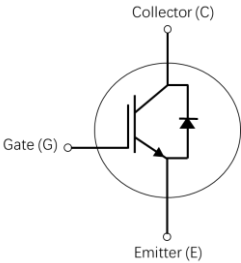
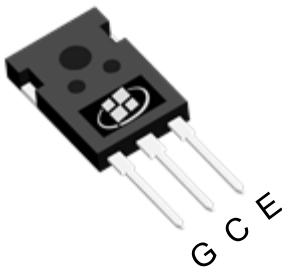
- Easy paralleling capability due to positive temperature coefficient in V_{CEsat}
- Low EMI
- Low Gate Charge Q_G
- Very soft, fast recovery full current anti-parallel diode
- Maximum junction temperature $T_{VJMAX}=175^{\circ}C$
- RoHS compliant ^(Note 1)
- Halogen-free ^(Note 1)

Applications:

- Industrial UPS
- Charger
- Energy Storage
- Three-phase Solar String Inverter

Key Performance Parameters:

Parameter	Value	Unit
V_{CE}	1200	V
$V_{CESAT}, T_{VJ} = 25^{\circ}C$	1.7	V
I_C	100	A



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKBK2A100YHH	TO247Plus-3L	BK2A100YHH	Tube	300PCS

Notes:

1. Contact ALKAIDSEMI sales for detail information

Maximum Ratings ($T_{VJ} = 25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{CE}	Collector-Emitter Voltage	1200	V
I_C	Collector Current - Continuous ($T_C = 25^{\circ}\text{C}$) ^(Note 1)	200	A
	Collector Current - Continuous ($T_C = 100^{\circ}\text{C}$)	100	A
I_{CM}	Collector Current - Pulsed ^(Note 2)	400	A
I_F	Diode Forward Current, Limited by T_{VJmax} ($T_C = 25^{\circ}\text{C}$)	200	A
	Diode Forward Current, Limited by T_{VJmax} ($T_C = 100^{\circ}\text{C}$)	100	A
I_{FM}	Diode Pulsed Current, - Pulsed ^(Note 2)	400	A
V_{GE}	Gate - Emitter Voltage	± 20	V
	Transient Gate-Emitter Voltage ($t_p \leq 10\mu\text{s}$, $D < 0.010$)	± 30	
P_D	Power Dissipation ($T_C = 25^{\circ}\text{C}$)	1154	W
	Power Dissipation ($T_C = 100^{\circ}\text{C}$)	577	W
T_J	Operating Junction Temperature Range	-40 to +175	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	IGBT Thermal Resistance, Junction-to-Case, Steady-State	0.13	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Diode Thermal Resistance, Junction-to-Case, Steady-State	0.26	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient Steady State	40	$^{\circ}\text{C/W}$

Notes:

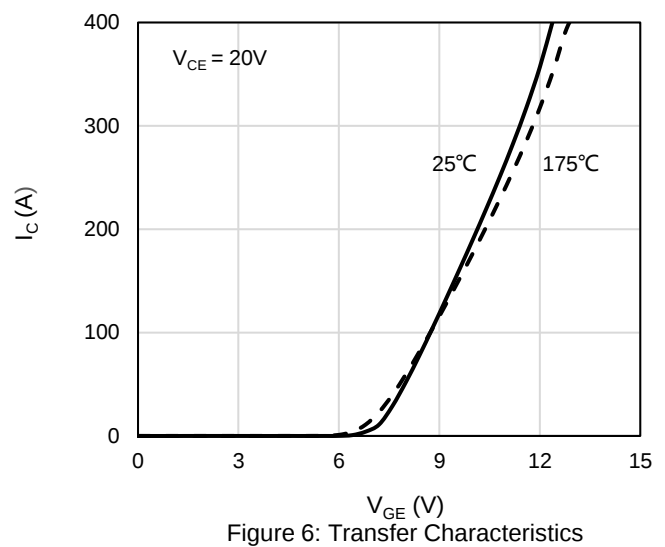
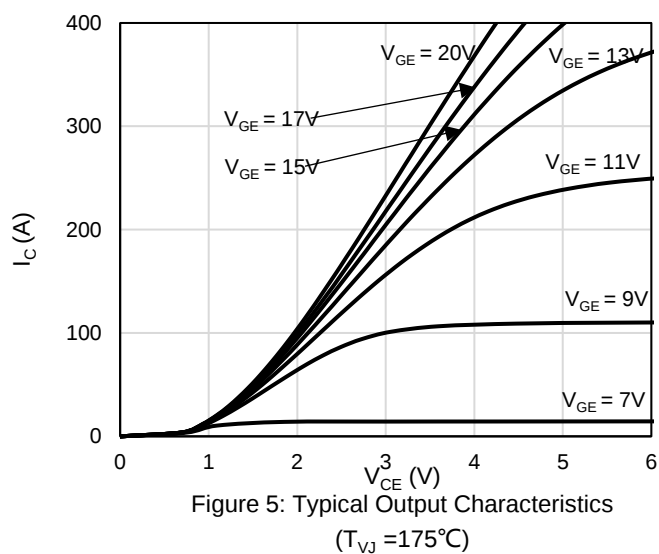
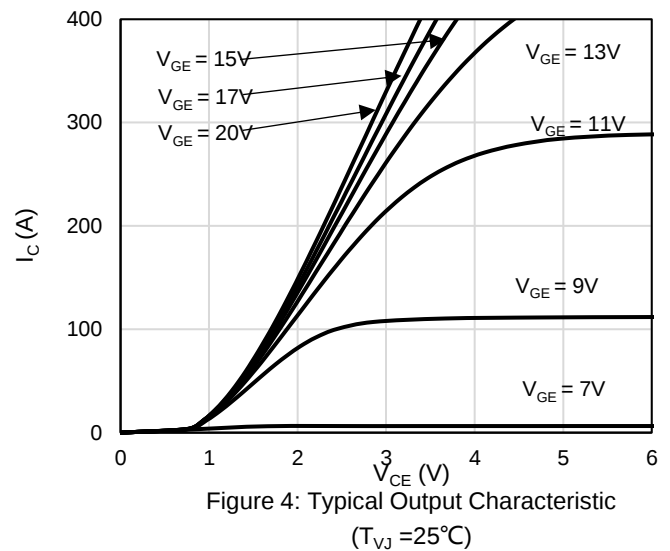
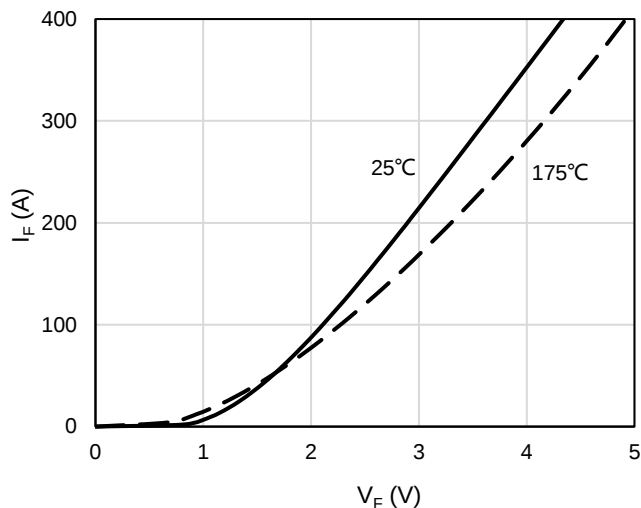
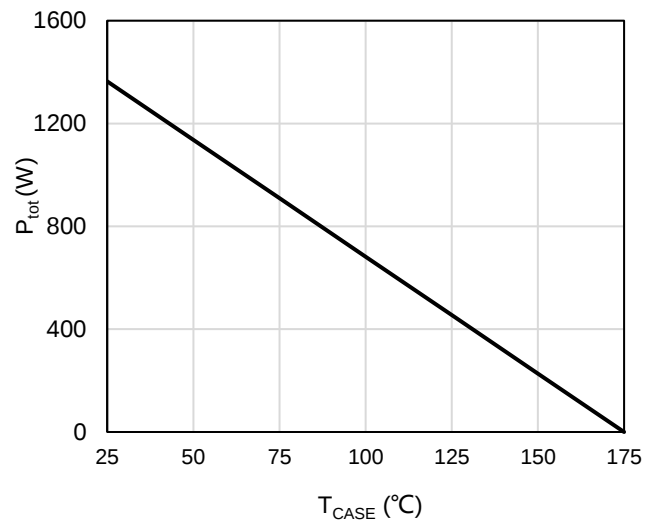
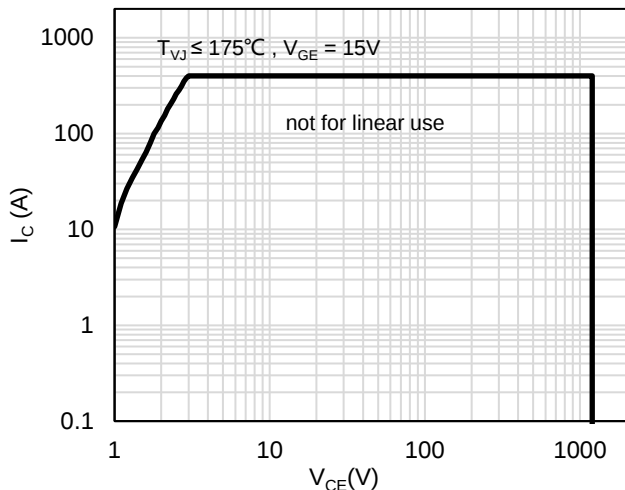
1. The max collector current rating is package limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature

Electrical Characteristics ($T_{VJ} = 25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage	$V_{GE} = 0\text{ V}, I_C = 0.5\text{ mA}$	1200			V
V_{CESAT}	Collector-Emitter Saturation Voltage	$V_{GE} = 15\text{ V}, I_C = 100\text{ A}$		1.70	2.15	V
		$V_{GE} = 15\text{ V}, I_C = 100\text{ A}, T_{VJ} = 175^{\circ}\text{C}$		2.40		
V_F	Diode Forward Voltage	$V_{GE} = 0\text{ V}, I_F = 100\text{ A}$		2.10	2.50	V
		$V_{GE} = 0\text{ V}, I_F = 100\text{ A}, T_{VJ} = 175^{\circ}\text{C}$		2.20		
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	$V_{CE} = V_{GE}, I_C = 3.5\text{ mA}$	5.1	5.8	6.5	V
I_{CES}	Zero Gate Voltage Collector Current	$V_{CE} = 1200\text{ V}, V_{GE} = 0\text{ V}$			40	μA
		$V_{CE} = 1200\text{ V}, V_{GE} = 0\text{ V}, T_{VJ} = 175^{\circ}\text{C}$		5000		
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 20\text{ V}, V_{CE} = 0\text{ V}$			± 100	nA
g_{fs}	Transconductance	$V_{GE} = 20\text{ V}, I_C = 100\text{ A}$		48		S
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V},$ $F = 1\text{ MHz}$		13992		pF
C_{oes}	Output Capacitance			383		pF
C_{res}	Reverse Transfer Capacitance			88		pF
Q_G	Total Gate Charge	$V_{CC} = 960\text{ V}, I_C = 100\text{ A},$ $V_{GE} = 15\text{ V}$		460		nC
Q_{GE}	Gate-Emitter Charge			97		nC
Q_{GC}	Gate-Collector Charge			165		nC
L_E	Internal Emitter Inductance			13		nH
Switching Characteristics, Inductive Load ($T_{VJ} = 25^{\circ}\text{C}$)						
$t_{d(on)}$	Turn On Delay Time	$V_{CC} = 600\text{ V}$ $I_C = 100\text{ A}$ $V_{GE} = 15\text{ V}$ $R_{G(on)} = 7\ \Omega$ $R_{G(off)} = 7\ \Omega$		64		ns
t_r	Rise Time			151		ns
$t_{d(off)}$	Turn Off Delay Time			280		ns
t_f	Fall Time			97		ns
E_{on}	Turn On Energy			9.07		mJ
E_{off}	Turn Off Energy			4.15		mJ
E_{total}	Total Switching Energy			13.22		mJ

Diode Characteristics						
t _{rr}	Reverse Recovery Time	V _R = 600 V, I _R = 100 A, di/dt = 1300 A/μs		278		ns
Q _{RR}	Reverse Recovery Charge			2.92		μC
I _{rrm}	Peak Reverse Recovery Current			32.6		A
dirr/dt	Diode Peak Rate of Fall of Reverse Recovery Current			110		A/μs
Switching Characteristics, Inductive Load (T _{VJ} = 175℃)						
t _{d(on)}	Turn On Delay Time	V _{CC} = 600 V I _C = 100 A V _{GE} = 15 V R _{G(on)} = 7 Ω R _{G(off)} = 7 Ω		62		ns
t _r	Rise Time			147		ns
t _{d(off)}	Turn Off Delay Time			310		ns
t _f	Fall Time			180		ns
E _{on}	Turn On Energy			13.5		mJ
E _{off}	Turn Off Energy			6.9		mJ
E _{total}	Total Switching Energy			20.4		mJ
Diode Characteristics						
t _{rr}	Reverse Recovery Time	V _R = 600 V, I _R = 100 A, di/dt = 1300 A/μs		490		ns
Q _{RR}	Reverse Recovery Charge			9.7		μC
I _{rrm}	Peak Reverse Recovery Current			50		A
dirr/dt	Diode Peak Rate of Fall of Reverse Recovery Current			95		A/μs

Electrical Characteristics Diagrams



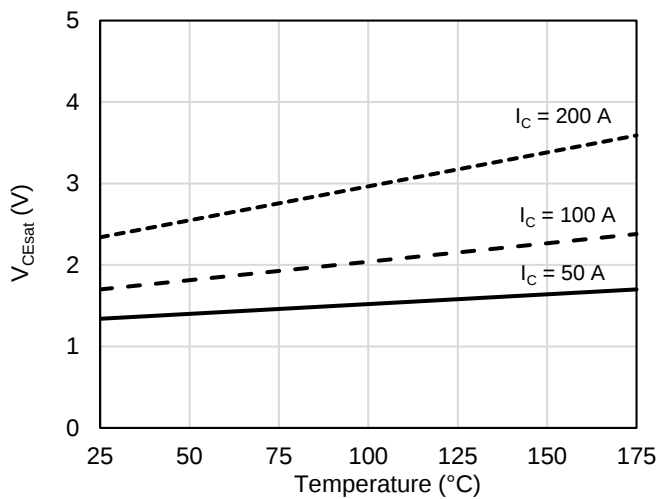


Figure 7: Collect-emitter Saturation Voltage vs. Junction Temperature

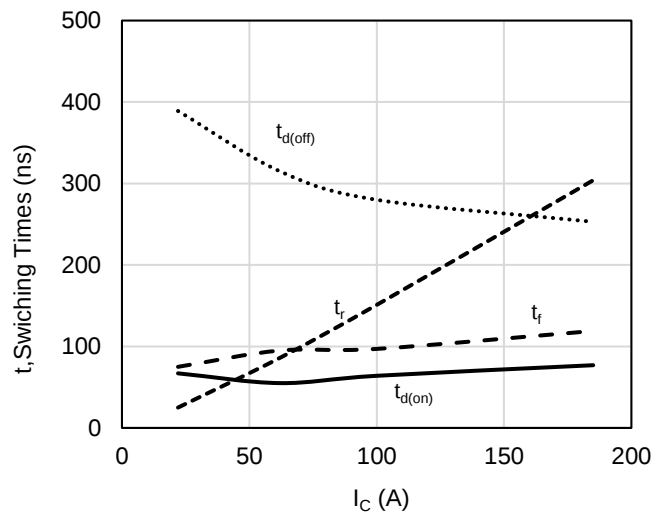


Figure 8: Switching Times vs. Collector Current

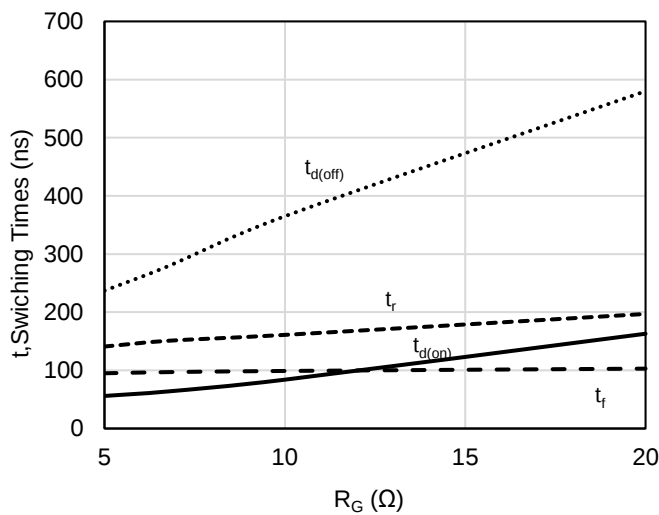


Figure 9: Switching Times vs. Gate Resistor

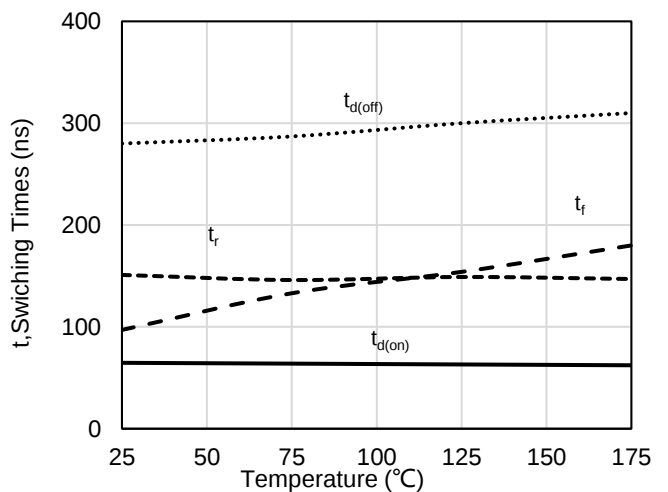


Figure 10: Switching Times vs. Junction Temperature

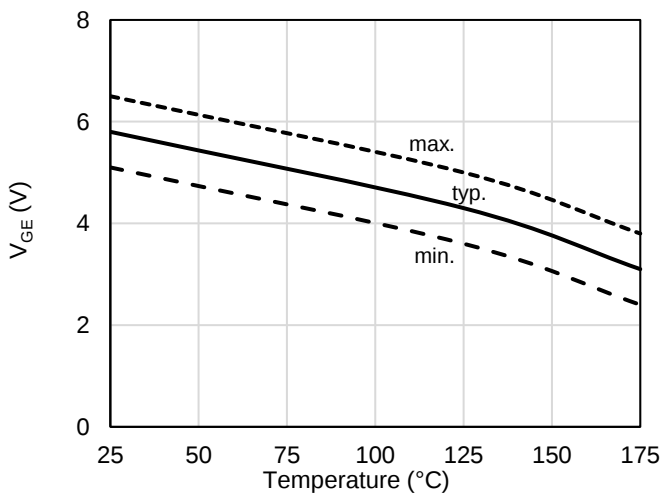


Figure 11: Threshold voltage vs. Junction Temperature

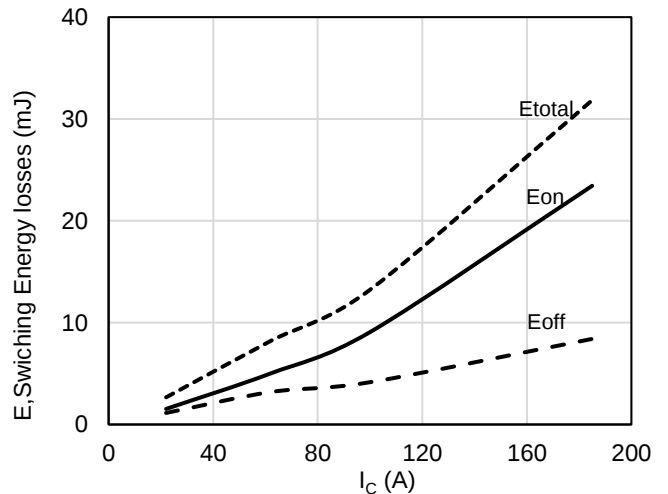
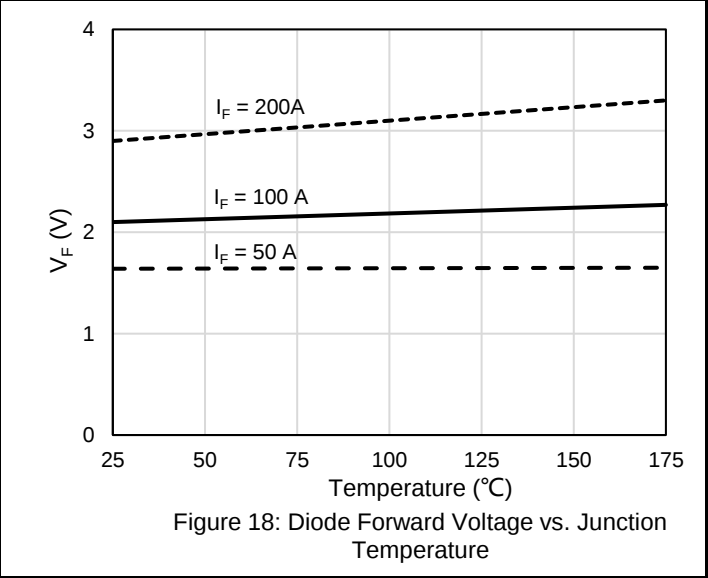
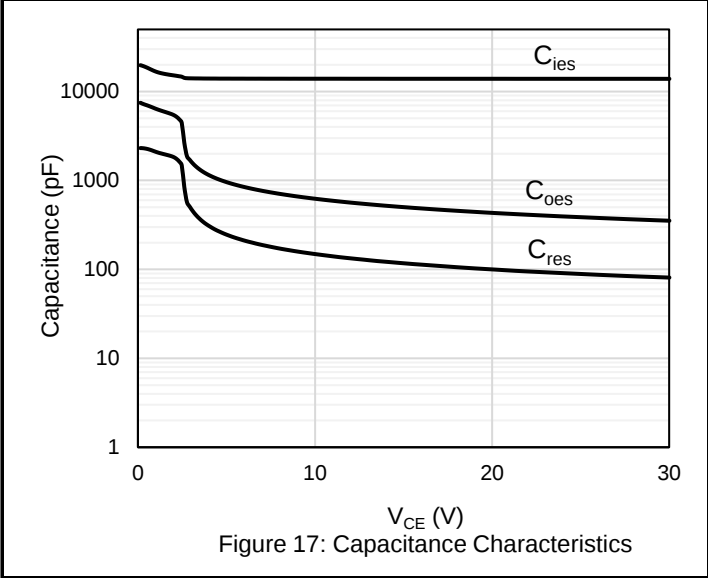
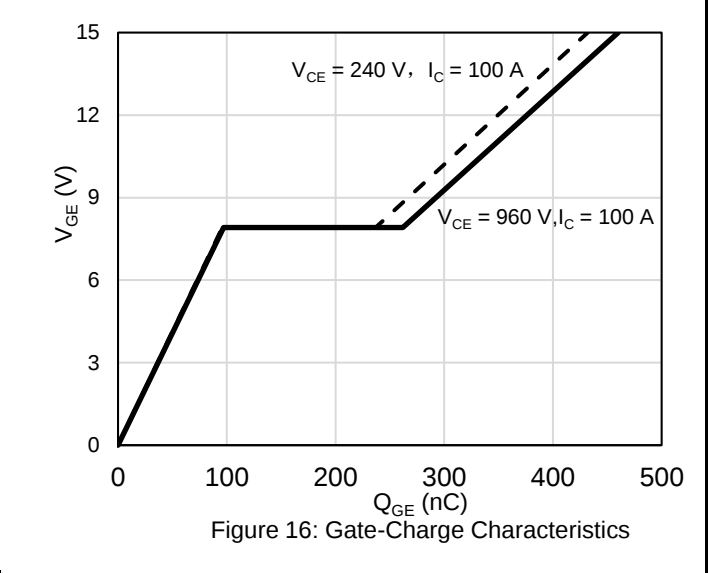
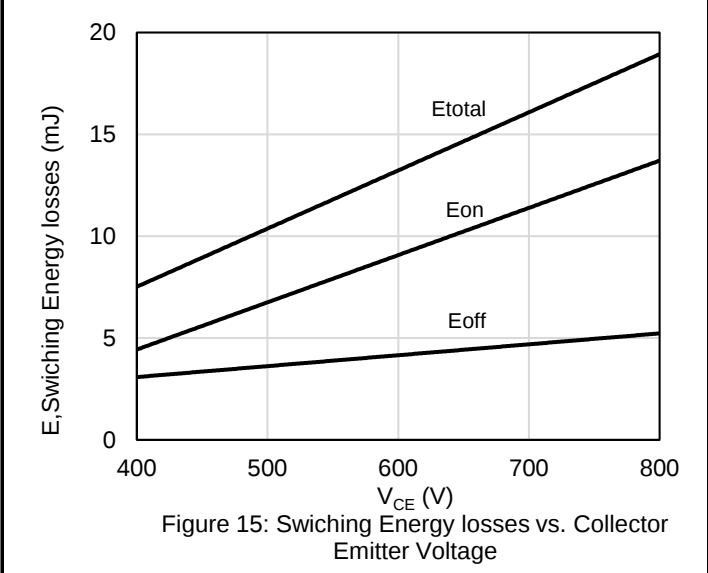
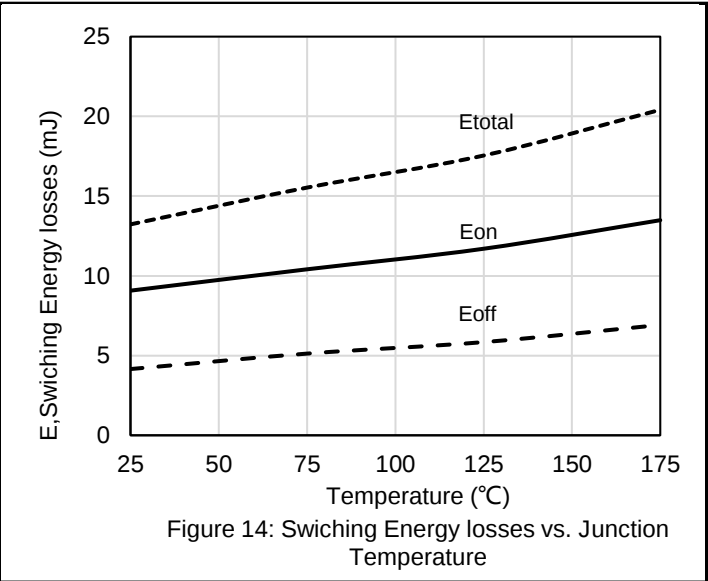
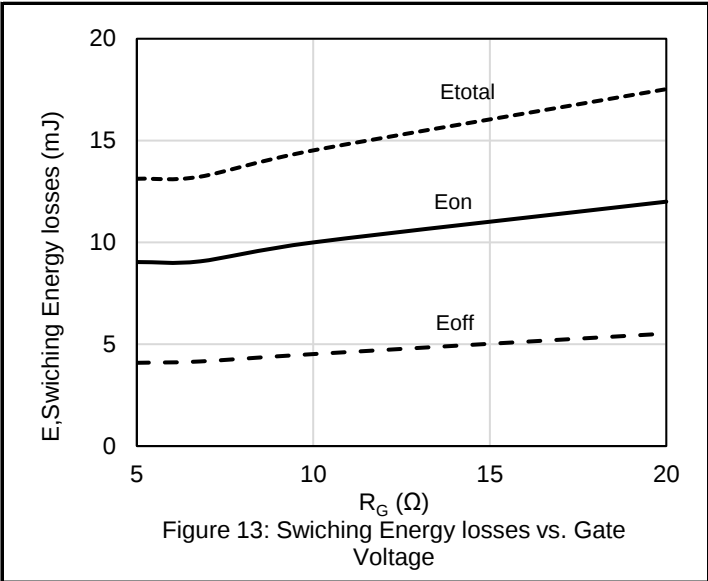


Figure 12: Switching Energy losses vs. Collector Current



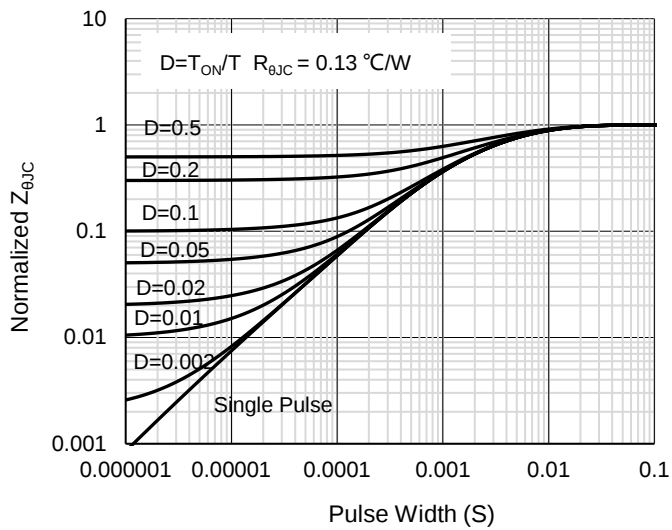


Figure 19: Normalized Maximum IGBT Transient Thermal Impedance

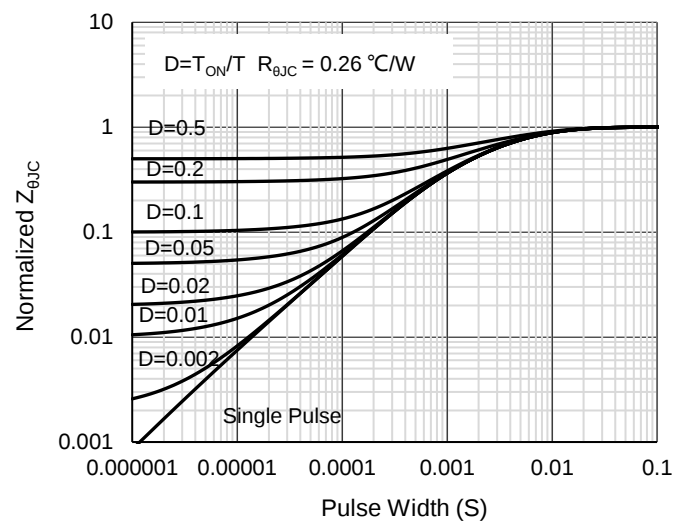


Figure 20: Normalized Maximum Diode Transient Thermal Impedance

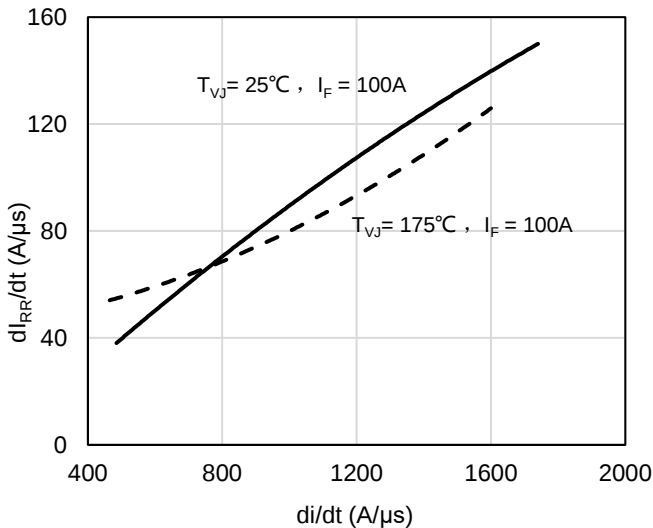


Figure 21: Diode Peak Rate of Fall of Reverse Recovery Current vs. Diode Current Slope

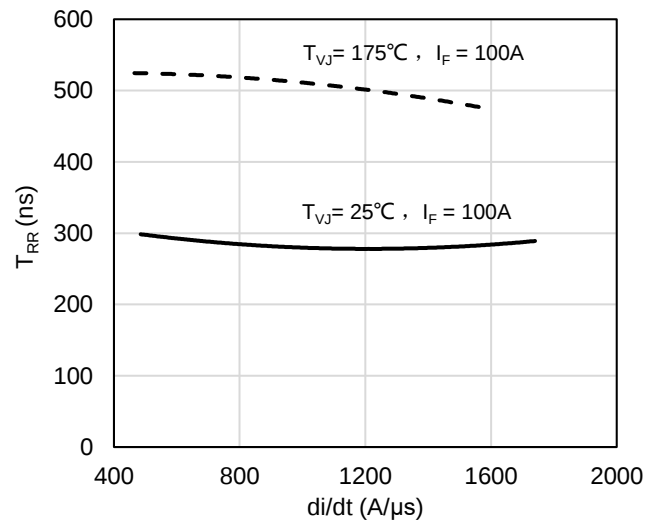


Figure 22: Reverse Recovery Time vs. Diode Current Slope

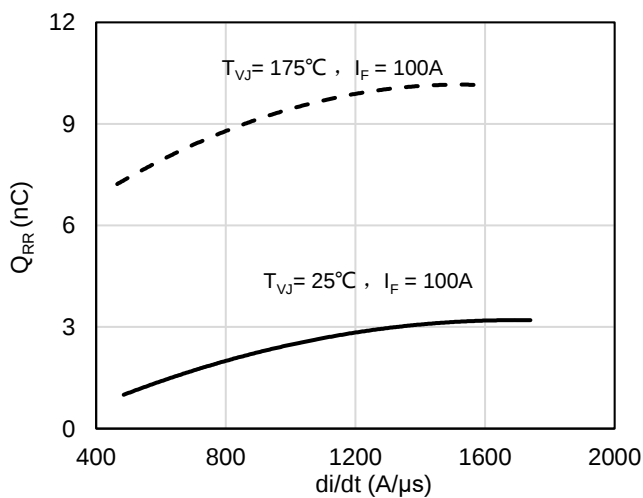


Figure 23: Reverse Recovery Charge vs. Diode Current Slope

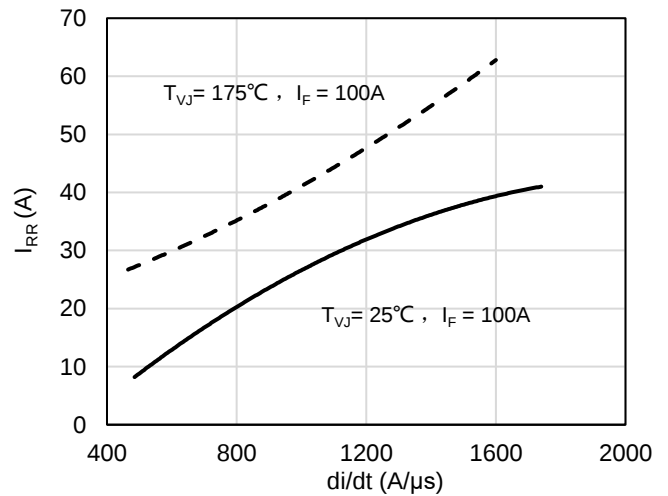
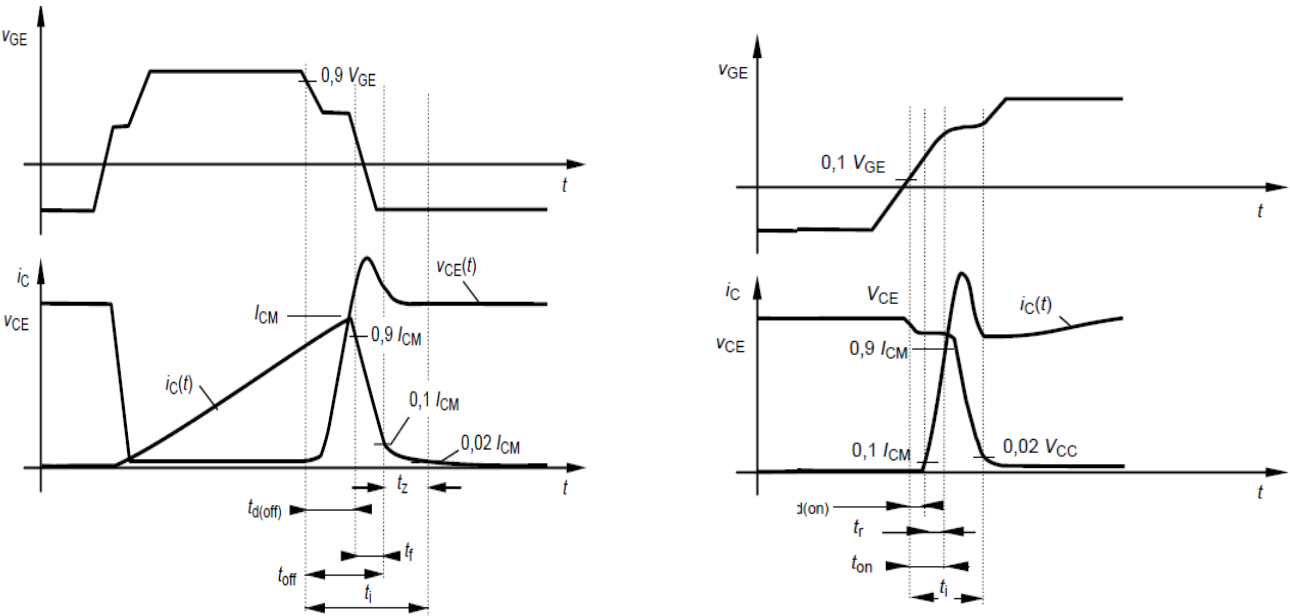


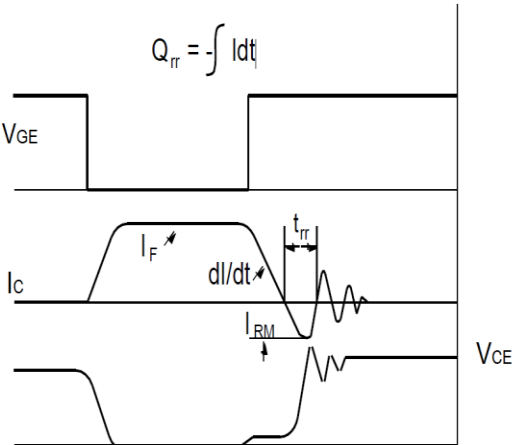
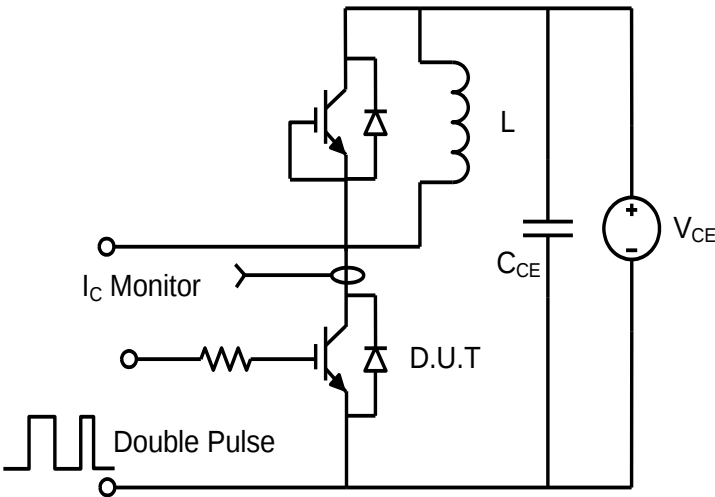
Figure 24: Reverse Recovery Current vs. Diode Current Slope

Test Circuit and Waveform

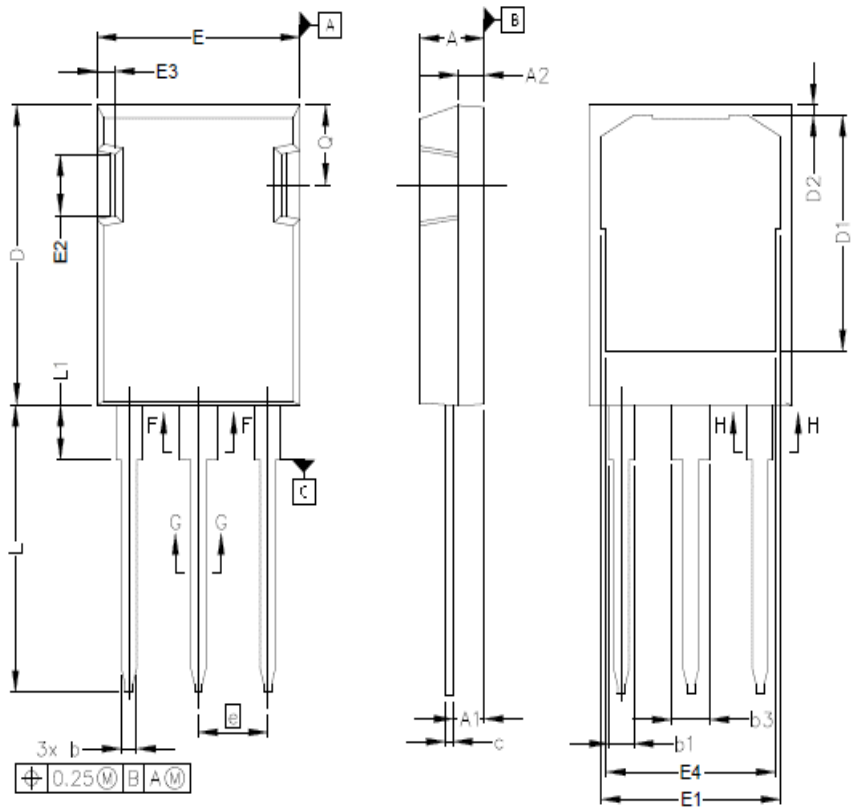
Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outlines



SYMBOL	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	1.91	2.41
b2	1.91	2.16
b3	2.87	3.38
b4	2.87	3.13
c'	0.55	0.65
c	0.55	0.68
D	20.80	21.10
D1	16.25	17.65
D2	0.50	0.80
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	5.44 BSC	
N	3	
L	19.81	20.32
L1	3.70	4.00
Q	5.49	6.00

Marking Information



BK2A100YHH

XXXXXXX

Note:

BK2A100YHH = Product Name Code

XXXXXXX = Date code

Contact ALKAIDSEMI sales for detail information

Revision History

Revision	Release Date	Remark
Rev.1.2	2023/8/22	

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.